

# DSFP Pin Map & Control Signals

SFP+ MSA Public		SFP+ MSA Public		1/29/2018	DSFP Finisar Proposal		DSFP Finisar Proposal	
Bottom		Top		Chris Cole, Finisar	Bottom		Top	
		20	VeeT		22	GND	20	GND
1	VeeT	19	TD-		1	TD2-	19	TD1-
2	TX_FAULT	18	TD+		2	TD2+	18	TD1+
3	TX_Disable	17	VeeT	VeeT = GND	3	GND	17	GND
4	SDA	16	VccT	VccT = VCC	4	SDA	16	VCC
5	SCL	15	VccR	VccR = VCC	5	SCL	15	VCC
6	Mod_ABS	14	VeeR	Mod_ABS = PRSn	6	LPWn/PRSn	14	GND
7	RS0	13	RD+	DSFP Pins 6 & 21: BiDi I/O, same as OSFP MSA	7	GND	13	RD1+
8	Rx_LOS	12	RD-		8	RD2+	12	RD1-
9	RS1	11	VeeR	VeeR = GND	9	RD2-	11	GND
10	VeeR				10	GND	21	INT/RSTn

**Toward Host**



(all pin views from top)

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(all pin views from top)